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7/17/03
C.P.

Serial No.: 09/523,990 Art Unit 2876

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7 July 2003
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Stephen B. Ackerman, Reg. No. 37,761
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Signature
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Date

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Inventors:	Mou-Shiung Lin et al.	Date :	7 July 2003
Serial No.:	09/523,990	Art Unit:	2876
Filing Date:	13 March 2000	Examiner:	Daniel J. Walsh
Confirmation Number	6138	Docket Number	MEG99-005C
Title:	Method of Manufacture and Identification of Semiconductor Chip Marked for Identification with Internal Marking Indicia and Protection Thereof by Non-Black Layer and Device Produced Thereby	Attorney:	George O. Saile 28 Davis Avenue Poughkeepsie, New York, 12603

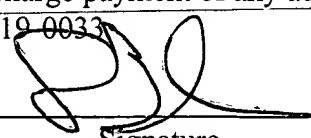
AMENDMENT

Commissioner for Patents
P. O. Box 1450
Alexandria VA 22313-1450

Your Honor:

07/17/2003 JARTIS 00000002 180033 09523990
01 FC:1251 110.00 DA
In response to the Office Action of 6 March 2003, please amend the above-identified application as follows:

The Commissioner is hereby authorized to charge payment of any additional fees involved with claims and the like to Deposit Account No. 19 0033

Stephen B. Ackerman, Reg. No. 37,761
Name

Signature
7 July 2003
Date

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